



Electronic Filing System (EFS) Data
Electronic Patent Application Submission
USPTO Use Only

EFS ID: 56632

Application ID: 10619638



Title of Invention: Content Addressable Memory
(CAM) Devices Having Error
Detection and Correction Control
Circuits Therein and Methods of
Operating Same

First Named Inventor: Chuen-Der Lien

Domestic/Foreign Application: Domestic Application

Filing Date: 2003-07-15

Effective Receipt Date: 2004-03-05

Submission Type: Information Disclosure
Statement

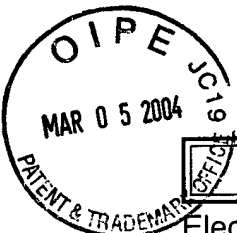
Filing Type:

Confirmation number: 1228

Attorney Docket Number: 5646-91IP


Total Fees Authorized:

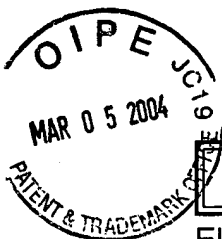
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TRANSMITTAL

Electronic Version v1.1
Stylesheet Version v1.1.0

Title of Invention	Content Addressable Memory (CAM) Devices Having Error Detection and Correction Control Circuits Therein and Methods of Operating Same						
<div style="display: flex; justify-content: space-between;"><div>Application Number: 10/619638</div><div></div></div> <div>Date: 2003-07-15</div> <div>First Named Applicant: Chuen-Der</div> <div>Confirmation Number: 1228</div> <div>Attorney Docket Number: 5646-91IP</div>							
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Submitted by:	Elec. Sign.	Sign. Capacity					
Grant J. Scott Registered Number: 36925	/gjs/	Attorney					
<table style="width: 100%;"><tr><td style="width: 50%; vertical-align: top;">Documents being submitted us-ids</td><td style="width: 50%; vertical-align: top;">Files 5646-91IP-usidst.xml us-ids.dtd us-ids.xsl</td></tr></table>		Documents being submitted us-ids	Files 5646-91IP-usidst.xml us-ids.dtd us-ids.xsl				
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Comments							



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of
Invention

Content Addressable Memory (CAM) Devices Having Error
Detection and Correction Control Circuits Therein and
Methods of Operating Same

Application Number: 10/619638



Confirmation Number: 1228

First Named Applicant: Chuen-Der Lien

Attorney Docket Number: 5646-91IP

Art Unit: 2818

Search string: (6700810 or 6690595 or

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6700810	2004-03-02	Ichiriu et al.	B1	365	49
	2	6690595	2004-02-10	Srinivasan et al.	B1	365	49
	3	6618281	2003-09-09	Gordon	B1	365	49

Signature

Examiner Name	Date